

	L #	Hits	Search Text	DBs
1	L1	215755	(enabl\$3 activat\$3) near10 (part portion subsection section segment)	USPAT; US-PGPUB
2	L2	108531	(size width length) near5 (operand data)	USPAT; US-PGPUB
3	L4	184	1 near50 2	USPAT; US-PGPUB
4	L3	8	1 near10 (unit module) near20 2	USPAT; US-PGPUB
5	L6	103772	(enabl\$3 activat\$3) near10 (part portion subsection section segment)	EPO; JPO; DERWENT; IBM_TDB
6	L7	51122	(size width length) near5 (operand data)	EPO; JPO; DERWENT; IBM_TDB
7	L8	77	6 near50 7	EPO; JPO; DERWENT; IBM_TDB
8	L10	460187	(power energy) near10 (sav\$3 reduc\$4 low lower\$3)	USPAT; US-PGPUB
9	L11	25789	10 near50 (activat\$3 enabl\$3)	USPAT; US-PGPUB
10	L12	17	11 near99 2	USPAT; US-PGPUB
11	L13	259	alu near50 2	USPAT; US-PGPUB
12	L14	22	alu near20 (select\$5 activat\$3 enabl\$3) near50 2	USPAT; US-PGPUB
13	L16	3	((two four) adj2 ((arithmetic adj2 logic adj unit) alu)) near99 2	USPAT; US-PGPUB
14	L15	58	((plural plurality multiple multiplicity several number) adj2 ((arithmetic adj2 logic adj unit) alu)) near99 2	USPAT; US-PGPUB
15	L18	146	11 and ((activat\$3 enabl\$3) near50 2)	USPAT; US-PGPUB
16	L20	0	((plural plurality multiple multiplicity several number) adj2 ((arithmetic adj2 logic adj unit) alu)) near99 7	EPO; JPO; DERWENT; IBM_TDB
17	L21	0	((two four) adj2 ((arithmetic adj2 logic adj unit) alu)) near99 7	EPO; JPO; DERWENT; IBM_TDB

	Docum ent ID	U	Title	Current OR
1	US 20030 12861 4 A1	<input type="checkbox"/>	SEMICONDUCTOR MEMORY DEVICE HAVING REFRESH SIZE SETTING CIRCUIT	365/222
2	US 20030 12069 4 A1	<input type="checkbox"/>	Method and apparatus for use in booth-encoded multiplication	708/628
3	US 20030 07167 9 A1	<input type="checkbox"/>	Internal high voltage generation circuit capable of stably generating internal high voltage and circuit element therefor	327/538
4	US 20030 04365 3 A1	<input type="checkbox"/>	Semiconductor memory device	365/200
5	US 20030 03865 3 A1	<input type="checkbox"/>	Semiconductor integrated circuit device including logic gate that attains reduction of power consumption and high-speed operation	326/33
6	US 20030 03104 6 A1	<input type="checkbox"/>	Thin film magnetic memory device realizing both high-speed data reading operation and stable operation	365/171
7	US 20030 02612 5 A1	<input type="checkbox"/>	Thin film magnetic memory device including memory cells having a magnetic tunnel junction	365/158
8	US 20020 17636 2 A1	<input type="checkbox"/>	Method for retransmitting data through a reverse link in packet data communication system using automatic repeat request	370/236
9	US 20020 17431 9 A1	<input type="checkbox"/>	Method and apparatus for reducing logic activity in a microprocessor	712/200
10	US 20020 17218 6 A1	<input type="checkbox"/>	Instantaneous joint transmit power control and link adaptation for RTS/CTS based channel access	370/349
11	US 20020 17207 3 A1	<input type="checkbox"/>	Thin film magnetic memory device capable of conducting stable data read and write operations	365/158
12	US 20020 15929 9 A1	<input type="checkbox"/>	DATA TRANSFER CIRCUIT AND SEMICONDUCTOR INTEGRATED CIRCUIT HAVING THE SAME	365/189 .05
13	US 20020 15705 0 A1	<input type="checkbox"/>	Position independent testing of circuits	714/726
14	US 20020 14795 0 A1	<input type="checkbox"/>	Method and apparatus for test connectivity, communication, and control	714/726
15	US 20020 14109 7 A1	<input type="checkbox"/>	Disk apparatus and head position control method	360/73. 03
16	US 20020 12992 1 A1	<input type="checkbox"/>	Differential quench method and apparatus	164/414
17	US 20020 12248 3 A1	<input type="checkbox"/>	Encoding circuit and method	375/240 .03

	Docum ent ID	U	Title	Current OR
18	US 20020 11870 2 A1	<input type="checkbox"/>	Use of modified line encoding and low signal-to-noise ratio based signal processing to extend range of digital data transmission over repeaterless two-wire telephone link	370/466
19	US 20020 10459 7 A1	<input type="checkbox"/>	Method and apparatus for producing steel	148/541
20	US 20020 09762 6 A1	<input type="checkbox"/>	Semiconductor memory device	365/222
21	US 20020 08085 6 A1	<input type="checkbox"/>	Process and device for the generation of codes, for example channeling codes for CDMA applications	375/140
22	US 20020 07527 2 A1	<input type="checkbox"/>	Display control device and mobile electronic apparatus	345/531
23	US 20020 07268 3 A1	<input type="checkbox"/>	Implantable medical device responsive to heart rate variability analysis	600/515
24	US 20020 06887 5 A1	<input type="checkbox"/>	Implantable medical device responsive to heart rate variability analysis	600/515
25	US 20020 06326 2 A1	<input type="checkbox"/>	Semiconductor apparatus having a large-size bus connection	257/200
26	US 20020 04818 2 A1	<input type="checkbox"/>	Memory circuit/logic circuit integrated device capable of reducing term of works	365/63
27	US 20020 03103 5 A1	<input type="checkbox"/>	Multi-bank semiconductor memory device	365/230 .03
28	US 20020 01534 4 A1	<input type="checkbox"/>	High-speed read-write circuitry for semi-conductor memory devices	365/207
29	US 20020 00124 6 A1	<input type="checkbox"/>	Semiconductor memory device	365/222
30	US 20010 05546 7 A1	<input type="checkbox"/>	Digital video recording apparatus	386/46
31	US 20010 05279 2 A1	<input type="checkbox"/>	Semiconductor integrated circuit device including logic gate that attains reduction of power consumption and high-speed operation	326/33
32	US 20010 02957 2 A1	<input type="checkbox"/>	Semiconductor memory device having cache function	711/118
33	US 20010 01952 2 A1	<input type="checkbox"/>	Information recording/reproducing method and apparatus using EPRML connection processing system	369/47. 19
34	US 20010 01398 9 A1	<input type="checkbox"/>	DISK APPARATUS WITH REAL-TIME ECCENRICITY CORRECTION	360/78. 04

	Docum ent ID	U	Title	Current OR
35	US 20010 01223 1 A1	<input type="checkbox"/>	Memory circuit/logic circuit integrated device capable of reducing term of works	365/230 .01
36	US 20010 00154 5 A1	<input type="checkbox"/>	Internal high voltage generation circuit capable of stably generating internal high voltage and circuit element therefor	327/541
37	US 65711 22 B2	<input type="checkbox"/>	Implantable medical device responsive to heart rate variability analysis	600/515
38	US 65711 21 B2	<input type="checkbox"/>	Implantable medical device responsive to heart rate variability analysis	600/515
39	US 65576 22 B2	<input type="checkbox"/>	Differential quench method and apparatus	164/455
40	US 65534 53 B1	<input type="checkbox"/>	Variable width content addressable memory device for searching variable width data	711/108
41	US 65389 53 B2	<input type="checkbox"/>	Semiconductor memory device	365/230 .03
42	US 64932 74 B2	<input type="checkbox"/>	Data transfer circuit and semiconductor integrated circuit having the same	365/189 .05
43	US 64928 63 B2	<input type="checkbox"/>	Internal high voltage generation circuit capable of stably generating internal high voltage and circuit element therefor	327/538
44	US 64831 65 B2	<input type="checkbox"/>	Semiconductor integrated circuit device including logic gate that attains reduction of power consumption and high-speed operation	257/499
45	US 64799 56 B1	<input type="checkbox"/>	Brushless motor control device and equipment using the control device	318/254
46	US 64770 75 B2	<input type="checkbox"/>	Memory circuit/logic circuit integrated device capable of reducing term of works	365/63
47	US 64629 98 B1	<input type="checkbox"/>	Programmable and electrically configurable latch timing circuit	365/205
48	US 64625 84 B1	<input type="checkbox"/>	GENERATING A TAIL CURRENT FOR A DIFFERENTIAL TRANSISTOR PAIR USING A CAPACITIVE DEVICE TO PROJECT A CURRENT FLOWING THROUGH A CURRENT SOURCE DEVICE ONTO A NODE HAVING A DIFFERENT VOLTAGE THAN THE CURRENT SOURCE DEVICE	327/52
49	US 64180 67 B1	<input type="checkbox"/>	Semiconductor memory device suitable for merging with logic	365/200
50	US 64178 46 B1	<input type="checkbox"/>	Multifunction input device	345/173
51	US 64053 35 B1	<input type="checkbox"/>	Position independent testing of circuits	714/726
52	US 64011 98 B1	<input type="checkbox"/>	STORING SYSTEM-LEVEL MASS STORAGE CONFIGURATION DATA IN NON-VOLATILE MEMORY ON EACH MASS STORAGE DEVICE TO ALLOW FOR REBOOT/POWER-ON RECONFIGURATION OF ALL INSTALLED MASS STORAGE DEVICES TO THE SAME CONFIGURATION AS LAST USE	713/1
53	US 63930 29 B1	<input type="checkbox"/>	Use of modified line encoding and low signal-to-noise auto ratio based signal processing to extend range of digital data transmission over repeaterless two-wire telephone link	370/419
54	US 63780 93 B1	<input type="checkbox"/>	Controller for scan distributor and controller architecture	714/726
55	US 63749 01 B1	<input type="checkbox"/>	Differential quench method and apparatus	164/414

	Docum ent ID	U	Title	Current OR.
56	US 63737 53 B1	<input type="checkbox"/>	Memory array having selected word lines driven to an internally-generated boosted voltage that is substantially independent of VDD	365/189 .09
57	US 63665 15 B2	<input type="checkbox"/>	Semiconductor memory device	365/222
58	US 63564 85 B1	<input type="checkbox"/>	Merging write cycles by comparing at least a portion of the respective write cycle addresses	365/189 .01
59	US 63358 75 B1	<input type="checkbox"/>	Memory circuit/logic circuit integrated device capable of reducing term of works	365/63
60	US 63339 96 B1	<input type="checkbox"/>	Handwriting code processing device enabling reduction in load on processing of handwriting input device and processing method thereof	382/189
61	US 63241 10 B1	<input type="checkbox"/>	High-speed read-write circuitry for semi-conductor memory	365/207
62	US 63075 07 B1	<input type="checkbox"/>	System and method for multi-mode operation of satellite phased-array antenna	342/373
63	US 63011 87 B1	<input type="checkbox"/>	Synchronous type semiconductor memory device permitting reduction in ratio of area occupied by control circuit in chip area	365/233
64	US 62920 15 B1	<input type="checkbox"/>	Semiconductor integrated circuit device including logic gate that attains reduction of power consumption and high-speed operation	326/33
65	US 62821 35 B1	<input type="checkbox"/>	Intializing memory cells within a dynamic memory array prior to performing internal memory operations	365/203
66	US 62647 67 B1	<input type="checkbox"/>	Method of producing martensite-or bainite-rich steel using steckel mill and controlled cooling	148/547
67	US 62596 41 B1	<input type="checkbox"/>	Integrated memory having sense amplifiers disposed on opposite sides of a cell array	365/205
68	US 62567 07 B1	<input type="checkbox"/>	Semiconductor memory device having cache function	711/118
69	US 62400 46 B1	<input type="checkbox"/>	Integrated circuit random access memory capable of reading either one or more than one data word in a single clock cycle	365/233
70	US 62160 69 B1	<input type="checkbox"/>	Hidden acquisition module for acquiring data from a vehicle	701/36
71	US 62157 44 B1	<input type="checkbox"/>	Information recording/reproducing method and apparatus using EPRML connection processing system	369/47. 24
72	US 62121 09 B1	<input type="checkbox"/>	Dynamic memory array having write data applied to selected bit line sense amplifiers before sensing to write associated selected memory cells	365/190
73	US 62085 75 B1	<input type="checkbox"/>	Dynamic memory array bit line sense amplifier enabled to drive toward, but stopped before substantially reaching, a source of voltage	365/208
74	US 62085 59 B1	<input type="checkbox"/>	Method of operating EEPROM memory cells having transistors with thin gate oxide and reduced disturb	365/185 .18
75	US 62085 47 B1	<input type="checkbox"/>	Memory circuit/logic circuit integrated device capable of reducing term of works	365/51
76	US 62014 37 B1	<input type="checkbox"/>	Internal high voltage generation circuit capable of stably generating internal high voltage and circuit element therefor	327/545
77	US 61986 82 B1	<input type="checkbox"/>	Hierarchical dynamic memory array architecture using read amplifiers separate from bit line sense amplifiers	365/207
78	US 61816 33 B1	<input type="checkbox"/>	Semiconductor device	365/230 .03

	Docum ent ID	U	Title	Current OR
79	US 61634 75 A	<input type="checkbox"/>	Bit line cross-over layout arrangement	365/63
80	US 61448 78 A	<input type="checkbox"/>	Implantable medical device responsive to heart rate variability analysis	600/515
81	US 61376 46 A	<input type="checkbox"/>	Disk apparatus using coincidence detection to generate variable sector pulses	360/51
82	US 61046 53 A	<input type="checkbox"/>	Equilibration circuit and method using a pulsed equilibrate signal and a level equilibrate signal	365/203
83	US 60882 84 A	<input type="checkbox"/>	Memory chip having multiple input/output system	365/230 .03
84	US 60781 96 A	<input type="checkbox"/>	Data enabled logic circuits	326/121
85	US 60698 56 A	<input type="checkbox"/>	Information recording/reproduction method and apparatus using EPRML connection processing system using concatenated demodulators	369/47. 19
86	US 60497 21 A	<input type="checkbox"/>	Radio communication system including SDL having transmission rate of relatively high speed	455/509
87	US 60352 33 A	<input type="checkbox"/>	Implantable medical device responsive to heart rate variability analysis	600/515
88	US 60165 47 A	<input type="checkbox"/>	Data processing apparatus with program allocating section for selectively allocating programs in first and second memory	713/300
89	US 59995 42 A	<input type="checkbox"/>	Use of modified line encoding and low signal-to-noise ratio based signal processing to extend range of digital data transmission over repeaterless two-wire telephone link	370/497
90	US 59828 10 A	<input type="checkbox"/>	Signal extraction circuit and correlator utilizing the circuit	375/150
91	US 59633 98 A	<input type="checkbox"/>	Disk apparatus with voice coil motor	360/264 .7
92	US 59599 30 A	<input type="checkbox"/>	Multi-bank synchronous semiconductor memory device	365/230 .03
93	US 59497 36 A	<input type="checkbox"/>	Memory control circuit reducing a consumed power of memory	365/230 .06
94	US 59436 81 A	<input type="checkbox"/>	Semiconductor memory device having cache function	711/105
95	US 59205 11 A	<input type="checkbox"/>	High-speed data input circuit for a synchronous memory device	365/189 .05
96	US 58674 46 A	<input type="checkbox"/>	Synchronous semiconductor memory device	365/233
97	US 58601 07 A	<input type="checkbox"/>	Processor and method for store gathering through merged store operations	711/140
98	US 58449 90 A	<input type="checkbox"/>	Transmission-reception system	380/262
99	US 58354 48 A	<input type="checkbox"/>	Clock synchronous semiconductor memory device for determining an operation mode	365/233
100	US 58354 24 A	<input type="checkbox"/>	Semiconductor memory	365/200
101	US 58187 76 A	<input type="checkbox"/>	Semiconductor memory device and method of reading data therefrom	365/221

	Docum ent ID	U	Title	Current OR
102	US 58140 81 A	<input type="checkbox"/>	Atrial flutter cardioverter and method	607/5
103	US 58090 33 A	<input type="checkbox"/>	Use of modified line encoding and low signal-to-noise ratio based signal processing to extend range of digital data transmission over repeaterless two-wire telephone link	370/522
104	US 57988 85 A	<input type="checkbox"/>	Head positioning control for disk apparatus using peak detection, polarity detection and sector mark detection	360/77. 08
105	US 57549 61 A	<input type="checkbox"/>	Radio communication system including SDL having transmission rate of relatively high speed	455/517
106	US 57499 00 A	<input type="checkbox"/>	Implantable medical device responsive to heart rate variability analysis	607/4
107	US 57267 86 A	<input type="checkbox"/>	Free-space star-coupled optical data bus	398/128
108	US 57266 39 A	<input type="checkbox"/>	Method and device for off hour over the air software programming of selective call receivers	340/825 .22
109	US 57244 52 A	<input type="checkbox"/>	Quantization estimating method for data compression quantization estimating circuit of data compressing apparatus	382/251
110	US 57176 51 A	<input type="checkbox"/>	Semiconductor memory	365/233
111	US 57086 22 A	<input type="checkbox"/>	Clock synchronous semiconductor memory device	365/233
112	US 56896 55 A	<input type="checkbox"/>	Apparatus for connecting and disconnecting drives under active state	710/302
113	US 56733 65 A	<input type="checkbox"/>	Fuzzy microcontroller for complex nonlinear signal recognition	706/46
114	US 56509 78 A	<input type="checkbox"/>	Semiconductor memory device having data transition detecting function	365/233 .5
115	US 55947 04 A	<input type="checkbox"/>	Synchronous semiconductor memory device	365/233
116	US 55900 78 A	<input type="checkbox"/>	Method of and apparatus for improved dynamic random access memory (DRAM) providing increased data bandwidth and addressing range for current DRAM devices and/or equivalent bandwidth and addressing range for smaller DRAM devices	365/189 .01
117	US 55860 76 A	<input type="checkbox"/>	Semiconductor memory device permitting high speed data transfer and high density integration	365/203
118	US 55507 81 A	<input type="checkbox"/>	Semiconductor apparatus with two activating modes of different number of selected word lines at refreshing	365/222
119	US 55496 41 A	<input type="checkbox"/>	Atrial fibrillation type selective cardiovertor and method	607/4
120	US 55218 78 A	<input type="checkbox"/>	Clock synchronous semiconductor memory device	365/233
121	US 55174 62 A	<input type="checkbox"/>	Synchronous type semiconductor memory device operating in synchronization with an external clock signal	365/233
122	US 54576 55 A	<input type="checkbox"/>	Column redundance circuit configuration for a memory	365/200
123	US 54559 23 A	<input type="checkbox"/>	Memory system for loading peripherals on power up	711/154

	Docum ent ID	U	Title	Current OR
124	US 54327 47 A	<input type="checkbox"/>	Self-timing clock generator for precharged synchronous SRAM	365/203
125	US 54306 81 A	<input type="checkbox"/>	Memory cartridge and its memory control method	365/222
126	US 54043 38 A	<input type="checkbox"/>	Synchronous type semiconductor memory device operating in synchronization with an external clock signal	365/233
127	US 53847 45 A	<input type="checkbox"/>	Synchronous semiconductor memory device	365/230 .03
128	US 53273 94 A	<input type="checkbox"/>	Timing and control circuit for a static RAM responsive to an address transition pulse	365/233 .5
129	US 52988 16 A	<input type="checkbox"/>	Write circuit for CMOS latch and memory systems	327/427
130	US 52529 67 A	<input type="checkbox"/>	Reader/programmer for two and three wire utility data communications system	340/870 .02
131	US 52433 38 A	<input type="checkbox"/>	Two and three wire utility data communications system	340/870 .02
132	US 52373 22 A	<input type="checkbox"/>	Master-slave data transmission system employing a flexible single-wire bus	340/870 .13
133	US 51554 81 A	<input type="checkbox"/>	Two and three wire utility data communications system	340/870 .02
134	US 50292 71 A	<input type="checkbox"/>	Superregenerative detector	329/347
135	US 50239 05 A	<input type="checkbox"/>	Pocket data receiver with full page visual display	379/93. 17
136	US 50035 27 A	<input type="checkbox"/>	System for recording optical discs for use with optical disc playback only devices	369/100
137	US 49610 73 A	<input type="checkbox"/>	Battery saving apparatus and method providing optimum synchronization codeword detection	340/7.3 4
138	US 49357 36 A	<input type="checkbox"/>	r. f. Remote controller for electrical loads having resistive or complex impedances	340/825 .69
139	US 49156 35 A	<input type="checkbox"/>	Compact interactive training manikin system	434/396
140	US 47631 37 A	<input type="checkbox"/>	Two pass thermal printing	347/199
141	US 45904 84 A	<input type="checkbox"/>	Thermal recording head driving control system	347/195
142	US 45032 91 A	<input type="checkbox"/>	Repertory dialer with efficient data storage	379/357 .04
143	US 42557 89 A	<input type="checkbox"/>	Microprocessor-based electronic engine control system	701/108
144	US 42085 80 A	<input type="checkbox"/>	Lithology determination from the calcium and magnesium activation lines	250/262
145	US 41106 84 A	<input type="checkbox"/>	Method of detecting faults on low voltage distribution electric cables utilizing a plurality of transient recorders	324/522
146	US 40608 01 A	<input type="checkbox"/>	Method and apparatus for non-scan matrix addressing of bar displays	345/38

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1	L1	215755	(enabl\$3 activat\$3) near10 (part portion subsection section segment)	USPAT; US-PGPUB
2	L2	108531	(size width length) near5 (operand data)	USPAT; US-PGPUB
3	L4	184	1 near50 2	USPAT; US-PGPUB
4	L3	8	1 near10 (unit module) near20 2	USPAT; US-PGPUB
5	L6	103772	(enabl\$3 activat\$3) near10 (part portion subsection section segment)	EPO; JPO; DERWENT; IBM_TDB
6	L7	51122	(size width length) near5 (operand data)	EPO; JPO; DERWENT; IBM_TDB
7	L8	77	6 near50 7	EPO; JPO; DERWENT; IBM_TDB
8	L10	460187	(power energy) near10 (sav\$3 reduc\$4 low lower\$3)	USPAT; US-PGPUB
9	L11	25789	10 near50 (activat\$3 enabl\$3)	USPAT; US-PGPUB
10	L12	17	11 near99 2	USPAT; US-PGPUB
11	L13	259	alu near50 2	USPAT; US-PGPUB
12	L14	22	alu near20 (select\$5 activat\$3 enabl\$3) near50 2	USPAT; US-PGPUB
13	L16	3	((two four) adj2 ((arithmetic adj2 logic adj unit) alu)) near99 2	USPAT; US-PGPUB
14	L15	58	((plural plurality multiple multiplicity several number) adj2 ((arithmetic adj2 logic adj unit) alu)) near99 2	USPAT; US-PGPUB
15	L18	146	11 and ((activat\$3 enabl\$3) near50 2)	USPAT; US-PGPUB

	Docum ent ID	U	Title	Current OR
1	US 20030 10579 3 A1	<input type="checkbox"/>	Long instruction word controlling plural independent processor operations	708/625
2	US 20020 12403 8 A1	<input checked="" type="checkbox"/>	Processor for processing variable length data	708/513
3	US 20020 10384 1 A1	<input checked="" type="checkbox"/>	Dynamically configurable processor	708/523
4	US 63705 58 B1	<input checked="" type="checkbox"/>	Long instruction word controlling plural independent processor operations	708/603
5	US 62404 37 B1	<input checked="" type="checkbox"/>	Long instruction word controlling plural independent processor operations	708/524
6	US 62196 88 B1	<input checked="" type="checkbox"/>	Method, apparatus and system for sum of plural absolute differences	708/709
7	US 61733 94 B1	<input checked="" type="checkbox"/>	Instruction having bit field designating status bits protected from modification corresponding to arithmetic logic unit result	712/226
8	US 61733 05 B1	<input checked="" type="checkbox"/>	Division by iteration employing subtraction and conditional source selection of a prior difference or a left shifted remainder	708/650
9	US 61167 68 A	<input checked="" type="checkbox"/>	Three input arithmetic logic unit with barrel rotator	708/236
10	US 60981 63 A	<input checked="" type="checkbox"/>	Three input arithmetic logic unit with shifter	712/20
11	US 60676 13 A	<input checked="" type="checkbox"/>	Rotation register for orthogonal data transformation	712/32
12	US 60584 73 A	<input checked="" type="checkbox"/>	Memory store from a register pair conditional upon a selected status bit	712/225
13	US 60321 70 A	<input checked="" type="checkbox"/>	Long instruction word controlling plural independent processor operations	708/620
14	US 60264 84 A	<input checked="" type="checkbox"/>	Data processing apparatus, system and method for if, then, else operation using write priority	712/226
15	US 60165 38 A	<input checked="" type="checkbox"/>	Method, apparatus and system forming the sum of data in plural equal sections of a single data word	712/32
16	US 59957 48 A	<input checked="" type="checkbox"/>	Three input arithmetic logic unit with shifter and/or mask generator	712/221
17	US 59957 47 A	<input checked="" type="checkbox"/>	Three input arithmetic logic unit capable of performing all possible three operand boolean operations with shifter and/or mask generator	712/221
18	US 59745 39 A	<input checked="" type="checkbox"/>	Three input arithmetic logic unit with shifter and mask generator	712/221
19	US 59616 35 A	<input checked="" type="checkbox"/>	Three input arithmetic logic unit with barrel rotator and mask generator	712/221
20	US 59601 93 A	<input checked="" type="checkbox"/>	Apparatus and system for sum of plural absolute differences	712/221
21	US 59037 79 A	<input checked="" type="checkbox"/>	System and method for efficient packing data into an output buffer	712/300
22	US 58059 13 A	<input checked="" type="checkbox"/>	Arithmetic logic unit with conditional register source selection	712/209

	Docum ent ID	U	Title	Current OR
23	US 57617 26 A	<input checked="" type="checkbox"/>	Base address generation in a multi-processing system having plural memories with a unified address space corresponding to each processor	711/147
24	US 57425 38 A	<input checked="" type="checkbox"/>	Long instruction word controlling plural independent processor operations	708/620
25	US 57348 80 A	<input checked="" type="checkbox"/>	Hardware branching employing loop control registers loaded according to status of sections of an arithmetic logic unit divided into a plurality of sections	712/221
26	US 57272 25 A	<input checked="" type="checkbox"/>	Method, apparatus and system forming the sum of data in plural equal sections of a single data word	712/224
27	US 57129 99 A	<input checked="" type="checkbox"/>	Address generator employing selective merge of two independent addresses	711/211
28	US 56969 59 A	<input checked="" type="checkbox"/>	Memory store from a selected one of a register pair conditional upon the state of a selected status bit	712/245
29	US 56969 54 A	<input checked="" type="checkbox"/>	Three input arithmetic logic unit with shifting means at one input forming a sum/difference of two inputs logically anded with a third input logically ored with the sum/difference logically anded with an inverse of the third input	712/221
30	US 56943 48 A	<input checked="" type="checkbox"/>	Method apparatus and system for correlation	708/525
31	US 56896 95 A	<input checked="" type="checkbox"/>	Conditional processor operation based upon result of two consecutive prior processor operations	712/234
32	US 56803 39 A	<input checked="" type="checkbox"/>	Method for rounding using redundant coded multiply result	708/493
33	US 56445 24 A	<input checked="" type="checkbox"/>	Iterative division apparatus, system and method employing left-most one's detection and left most one's detection with exclusive or	708/655
34	US 56445 22 A	<input checked="" type="checkbox"/>	Method, apparatus and system for multiply rounding using redundant coded multiply result	708/551
35	US 56405 78 A	<input checked="" type="checkbox"/>	Arithmetic logic unit having plural independent sections and register storing resultant indicator bit from every section	712/221
36	US 56340 65 A	<input checked="" type="checkbox"/>	Three input arithmetic logic unit with controllable shifter and mask generator	708/230
37	US 56066 77 A	<input checked="" type="checkbox"/>	Packed word pair multiply operation forming output including most significant bits of product and other bits of one input	712/208
38	US 56008 47 A	<input checked="" type="checkbox"/>	Three input arithmetic logic unit with mask generator	712/36
39	US 55967 63 A	<input checked="" type="checkbox"/>	Three input arithmetic logic unit forming mixed arithmetic and boolean combinations	708/670
40	US 55965 19 A	<input checked="" type="checkbox"/>	Iterative division apparatus, system and method employing left most one's detection and left most one's detection with exclusive OR	708/655
41	US 55903 50 A	<input checked="" type="checkbox"/>	Three input arithmetic logic unit with mask generator	712/36
42	US 55128 96 A	<input checked="" type="checkbox"/>	Huffman encoding method, circuit and system employing most significant bit change for size detection	341/65
43	US 55091 29 A	<input checked="" type="checkbox"/>	Long instruction word controlling plural independent processor operations	712/203
44	US 54935 24 A	<input checked="" type="checkbox"/>	Three input arithmetic logic unit employing carry propagate logic	708/709

	Docum ent ID	U	Title	Current OR
45	US 54854 11 A	<input checked="" type="checkbox"/>	Three input arithmetic logic unit forming the sum of a first input anded with a first boolean combination of a second input and a third input plus a second boolean combination of the second and third inputs	708/230
46	US 54791 66 A	<input checked="" type="checkbox"/>	Huffman decoding method, circuit and system employing conditional subtraction for conversion of negative numbers	341/65
47	US 54652 24 A	<input checked="" type="checkbox"/>	Three input arithmetic logic unit forming the sum of a first Boolean combination of first, second and third inputs plus a second Boolean combination of first, second and third inputs	708/236
48	US 54466 51 A	<input checked="" type="checkbox"/>	Split multiply operation	708/630
49	US 54425 81 A	<input checked="" type="checkbox"/>	Iterative division apparatus, system and method forming plural quotient bits per iteration	708/653
50	US 54208 09 A	<input checked="" type="checkbox"/>	Method of operating a data processing apparatus to compute correlation	708/200
51	US 52491 48 A	<input checked="" type="checkbox"/>	Method and apparatus for performing restricted modulo arithmetic	708/491
52	US 43239 81 A	<input checked="" type="checkbox"/>	Central processing unit with improved ALU circuit control	708/518
53	US 42939 07 A	<input checked="" type="checkbox"/>	Data processing apparatus having op-code extension register	712/208
54	US 42584 19 A	<input checked="" type="checkbox"/>	Data processing apparatus providing variable operand width operation	712/210
55	US 42505 45 A	<input checked="" type="checkbox"/>	Data processing apparatus providing autoloading of memory pointer registers	712/208
56	US 42478 93 A	<input checked="" type="checkbox"/>	Memory interface device with processing capability	710/305
57	US 42401 42 A	<input checked="" type="checkbox"/>	Data processing apparatus providing autoincrementing of memory pointer registers	712/42
58	US 41595 20 A	<input type="checkbox"/>	Memory address control device with extender bus	711/220

	L #	Hits	Search Text	DBs
1	L1	215755	(enabl\$3 activat\$3) near10 (part portion subsection section segment)	USPAT; US-PGPUB
2	L2	108531	(size width length) near5 (operand data)	USPAT; US-PGPUB
3	L4	184	1 near50 2	USPAT; US-PGPUB
4	L3	8	1 near10 (unit module) near20 2	USPAT; US-PGPUB
5	L6	103772	(enabl\$3 activat\$3) near10 (part portion subsection section segment)	EPO; JPO; DERWENT; IBM_TDB
6	L7	51122	(size width length) near5 (operand data)	EPO; JPO; DERWENT; IBM_TDB
7	L8	77	6 near50 7	EPO; JPO; DERWENT; IBM_TDB
8	L10	460187	(power energy) near10 (sav\$3 reduc\$4 low lower\$3)	USPAT; US-PGPUB
9	L11	25789	10 near50 (activat\$3 enabl\$3)	USPAT; US-PGPUB
10	L12	17	11 near99 2	USPAT; US-PGPUB
11	L13	259	alu near50 2	USPAT; US-PGPUB
12	L14	22	alu near20 (select\$5 activat\$3 enabl\$3) near50 2	USPAT; US-PGPUB
13	L16	3	((two four) adj2 ((arithmetic adj2 logic adj unit) alu)) near99 2	USPAT; US-PGPUB
14	L15	58	((plural plurality multiple multiplicity several number) adj2 ((arithmetic adj2 logic adj unit) alu)) near99 2	USPAT; US-PGPUB

	Docum ent ID	U	Title	Current OR
1	US 20030 12069 4 A1	<input type="checkbox"/>	Method and apparatus for use in booth-encoded multiplication	708/628
2	US 20030 02612 5 A1	<input checked="" type="checkbox"/>	Thin film magnetic memory device including memory cells having a magnetic tunnel junction	365/158
3	US 20020 14109 7 A1	<input checked="" type="checkbox"/>	Disk apparatus and head position control method	360/73. 03
4	US 20020 12248 3 A1	<input checked="" type="checkbox"/>	Encoding circuit and method	375/240 .03
5	US 20020 11870 2 A1	<input checked="" type="checkbox"/>	Use of modified line encoding and low signal-to-noise ratio based signal processing to extend range of digital data transmission over repeaterless two-wire telephone link	370/466
6	US 20010 02957 2 A1	<input checked="" type="checkbox"/>	Semiconductor memory device having cache function	711/118
7	US 63930 29 B1	<input checked="" type="checkbox"/>	Use of modified line encoding and low signal-to-noise auto ratio based signal processing to extend range of digital data transmission over repeaterless two-wire telephone link	370/419
8	US 62567 07 B1	<input checked="" type="checkbox"/>	Semiconductor memory device having cache function	711/118
9	US 61816 33 B1	<input checked="" type="checkbox"/>	Semiconductor device	365/230 .03
10	US 59995 42 A	<input checked="" type="checkbox"/>	Use of modified line encoding and low signal-to-noise ratio based signal processing to extend range of digital data transmission over repeaterless two-wire telephone link	370/497
11	US 59828 10 A	<input checked="" type="checkbox"/>	Signal extraction circuit and correlator utilizing the circuit	375/150
12	US 59436 81 A	<input checked="" type="checkbox"/>	Semiconductor memory device having cache function	711/105
13	US 58090 33 A	<input checked="" type="checkbox"/>	Use of modified line encoding and low signal-to-noise ratio based signal processing to extend range of digital data transmission over repeaterless two-wire telephone link	370/522
14	US 57244 52 A	<input checked="" type="checkbox"/>	Quantization estimating method for data compression quantization estimating circuit of data compressing apparatus	382/251
15	US 55860 76 A	<input checked="" type="checkbox"/>	Semiconductor memory device permitting high speed data transfer and high density integration	365/203
16	US 54559 23 A	<input checked="" type="checkbox"/>	Memory system for loading peripherals on power up	711/154
17	US 49610 73 A	<input type="checkbox"/>	Battery saving apparatus and method providing optimum synchronization codeword detection	340/7.3 4

	Docum ent ID	U	Title	Current OR
1	US 20030 03349 9 A1	<input type="checkbox"/>	Method and system for circular addressing with efficient memory usage	711/220
2	US 20020 10384 1 A1	<input checked="" type="checkbox"/>	Dynamically configurable processor	708/523
3	US 64635 60 B1	<input checked="" type="checkbox"/>	Method for implementing a bist scheme into integrated circuits for testing RTL controller-data paths in the integrated circuits	714/733
4	US 63857 17 B1	<input checked="" type="checkbox"/>	Programmable 1-bit data processing arrangement	712/225
5	US 58929 61 A	<input checked="" type="checkbox"/>	Field programmable gate array having programming instructions in the configuration bitstream	712/10
6	US 57064 65 A	<input checked="" type="checkbox"/>	Computers having cache memory	711/123
7	US 56894 50 A	<input checked="" type="checkbox"/>	Parallel processor	708/524
8	US 56799 13 A	<input checked="" type="checkbox"/>	Electronic apparatus for the automatic composition and reproduction of musical data	84/609
9	US 56690 12 A	<input checked="" type="checkbox"/>	Data processor and control circuit for inserting/extracting data to/from an optional byte position of a register	712/1
10	US 56196 68 A	<input checked="" type="checkbox"/>	Apparatus for register bypassing in a microprocessor	712/200
11	US 55347 99 A	<input checked="" type="checkbox"/>	Compact flag control circuit capable of producing a zero flag in a short time	327/20
12	US 55306 62 A	<input checked="" type="checkbox"/>	Fixed point signal processor having block floating processing circuitry	708/490
13	US 53136 01 A	<input checked="" type="checkbox"/>	Request control apparatus	711/214
14	US 53033 44 A	<input checked="" type="checkbox"/>	Protocol processing apparatus for use in interfacing network connected computer systems utilizing separate paths for control information and data transfer	709/230
15	US 52972 66 A	<input checked="" type="checkbox"/>	Apparatus and method for controlling memory requests in an information processor	711/214
16	US 49243 73 A	<input checked="" type="checkbox"/>	Apparatus and method for controlling switching elements in a PWN-controlled voltage source inverter	363/95
17	US 47853 93 A	<input checked="" type="checkbox"/>	32-Bit extended function arithmetic-logic unit on a single chip	712/221
18	US 43239 81 A	<input checked="" type="checkbox"/>	Central processing unit with improved ALU circuit control	708/518
19	US 42939 07 A	<input checked="" type="checkbox"/>	Data processing apparatus having op-code extension register	712/208
20	US 42584 19 A	<input checked="" type="checkbox"/>	Data processing apparatus providing variable operand width operation	712/210
21	US 42505 45 A	<input checked="" type="checkbox"/>	Data processing apparatus providing autoloading of memory pointer registers	712/208
22	US 42401 42 A	<input type="checkbox"/>	Data processing apparatus providing autoincrementing of memory pointer registers	712/42

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3	L4	184	1 near50 2	USPAT; US-PGPUB
4	L3	8	1 near10 (unit module) near20 2	USPAT; US-PGPUB
5	L6	103772	(enabl\$3 activat\$3) near10 (part portion subsection section segment)	EPO; JPO; DERWENT; IBM_TDB
6	L7	51122	(size width length) near5 (operand data)	EPO; JPO; DERWENT; IBM_TDB
7	L8	77	6 near50 7	EPO; JPO; DERWENT; IBM_TDB

	Docum ent ID	U	Title	Current OR
1	US 20030 12089 4 A1	<input type="checkbox"/>	Method of addressing sequential data packets from a plurality of input data line cards for shared memory storage and the like, and novel address generator therefor	711/220
2	US 20030 11752 6 A1	<input checked="" type="checkbox"/>	Image processing apparatus and television receiver using the same	348/445
3	US 20030 09941 0 A1	<input checked="" type="checkbox"/>	Image processing method and image processing apparatus	382/298
4	US 20030 09576 1 A1	<input checked="" type="checkbox"/>	SONET/sdh section tracer	385/100
5	US 20030 09023 0 A1	<input checked="" type="checkbox"/>	Synchronization control method and synchronization control device	318/625
6	US 20030 07183 2 A1	<input checked="" type="checkbox"/>	Adjustable display device with display adjustment function and method therefor	345/698
7	US 20030 06781 2 A1	<input checked="" type="checkbox"/>	Clock synchronous semiconductor memory device	365/189 .01
8	US 20030 06641 3 A1	<input checked="" type="checkbox"/>	Apparatus and method for detecting performer's motion to interactively control performance of music or the like	84/615
9	US 20030 05814 2 A1	<input checked="" type="checkbox"/>	Data compression techniques for reducing encoding bits	341/63
10	US 20030 04645 8 A1	<input checked="" type="checkbox"/>	Data transfer equipment and aligner included in data transfer equipment	710/22
11	US 20030 04108 6 A1	<input checked="" type="checkbox"/>	Method for synchronising program sections of a computer program	709/102
12	US 20030 03876 6 A1	<input checked="" type="checkbox"/>	Liquid crystal display and driving method thereof	345/87
13	US 20020 18559 7 A1	<input checked="" type="checkbox"/>	Scanning electron microscope with appropriate brightness control function	250/309
14	US 20020 17219 5 A1	<input checked="" type="checkbox"/>	Apparatus and method for disparate fabric data and transaction buffering within infiniband device	370/360
15	US 20020 16760 9 A1	<input checked="" type="checkbox"/>	TELEVISION CHANNEL SELECTION METHOD AND APPARATUS	348/554
16	US 20020 15234 5 A1	<input checked="" type="checkbox"/>	Data transfer equipment	710/305
17	US 20020 15017 2 A1	<input checked="" type="checkbox"/>	Caller-ID demodulation apparatus and method using multiple thresholds	375/300

	Docum ent ID	U	Title	Current OR
18	US 20020 13324 6 A1	<input checked="" type="checkbox"/>	Method of editing audio data and recording medium thereof and digital audio player	700/94
19	US 20020 11656 6 A1	<input checked="" type="checkbox"/>	Data transmitting and receiving system, and data receiving device	710/313
20	US 20020 09019 7 A1	<input checked="" type="checkbox"/>	Optical disc, recording apparatus, playback apparatus, program, computer-readable recording medium, recording method and playback method	386/52
21	US 20020 07619 8 A1	<input checked="" type="checkbox"/>	EDITING IMAGE DATA	386/55
22	US 20020 07290 0 A1	<input checked="" type="checkbox"/>	System and method of templating specific human voices	704/220
23	US 20020 05458 8 A1	<input checked="" type="checkbox"/>	System and method for controlling signal processing in a voice over packet (VoP) environment	370/352
24	US 20020 04475 9 A1	<input checked="" type="checkbox"/>	ON-LINE EDITING AND DATA CONVEYING MEDIA FOR EDIT DECISIONS	386/55
25	US 20020 04309 5 A1	<input checked="" type="checkbox"/>	Optical sensor and method of operation	73/1.02
26	US 20020 03680 0 A1	<input checked="" type="checkbox"/>	Information distribution system, information distribution device, information receiving device, information distribution method, information distribution program, recording medium recording the information distribution program, information receiving method, information receiving program, and recording medium recording the information receiving program	358/1.1 5
27	US 20020 02928 4 A1	<input checked="" type="checkbox"/>	System and method for providing random access to a multimedia object over a network	709/231
28	US 20020 02020 6 A1	<input checked="" type="checkbox"/>	Optical sensor and method of operation	73/1.02
29	US 20020 00047 4 A1	<input checked="" type="checkbox"/>	Memory card	235/492
30	US 20010 04977 5 A1	<input checked="" type="checkbox"/>	Communication device for rapidly and simultaneously writing onto a plurality of data carriers, and data carrier for such a communication device	711/154
31	US 20010 03964 2 A1	<input checked="" type="checkbox"/>	Design system for flip chip semiconductor device	716/10
32	US 20010 03610 6 A1	<input checked="" type="checkbox"/>	Nonvolatile semiconductor memory	365/185 .11
33	US 20010 03369 7 A1	<input checked="" type="checkbox"/>	Variable length coding unit and variable length decoding unit	382/246

	Docum ent ID	U	Title	Current OR
34	US 20010 02363 4 A1	<input checked="" type="checkbox"/>	Tone generating method and device	84/603
35	US 20010 01512 3 A1	<input checked="" type="checkbox"/>	Apparatus and method for detecting performer's motion to interactively control performance of music or the like	84/615
36	US 65938 61 B2	<input checked="" type="checkbox"/>	Data compression techniques for reducing encoding bits	341/61
37	US 65775 24 B2	<input checked="" type="checkbox"/>	Memory structures having selectively disabled portions for power conservation	365/78
38	US 65746 97 B2	<input checked="" type="checkbox"/>	Data transfer equipment that provides high speed data transmission between data terminal equipment and data circuit terminating equipment	710/305
39	US 65710 51 B2	<input checked="" type="checkbox"/>	Editing image data	386/55
40	US 65673 37 B1	<input checked="" type="checkbox"/>	Pulsed circuit topology to perform a memory array write operation	365/233
41	US 65642 96 B2	<input checked="" type="checkbox"/>	Communication device for rapidly and simultaneously writing onto a plurality of data carriers, and data carrier for such a communication device	711/115
42	US 65606 94 B1	<input checked="" type="checkbox"/>	Double prefix overrides to provide 16-bit operand size in a 32/64 operating mode	712/210
43	US 65602 19 B1	<input checked="" type="checkbox"/>	Hybrid exchange, an exchange, and a re-arrangement method for STM data in an exchange	370/352
44	US 65499 59 B1	<input checked="" type="checkbox"/>	Detecting modification to computer memory by a DMA device	710/22
45	US 65394 08 B1	<input checked="" type="checkbox"/>	Preconditioning of source data for packed min/max instructions	708/207
46	US 65360 11 B1	<input checked="" type="checkbox"/>	Enabling accurate demodulation of a DVD bit stream using devices including a SYNC window generator controlled by a read channel bit counter	714/814
47	US 65164 46 B2	<input checked="" type="checkbox"/>	Design system for flip chip semiconductor device	716/1
48	US 64900 58 B1	<input checked="" type="checkbox"/>	Image decoding and display device	358/442
49	US 64888 91 B2	<input checked="" type="checkbox"/>	Optical sensor and method of operation	422/58
50	US 64767 33 B1	<input checked="" type="checkbox"/>	Thin electronic data input device	341/20
51	US 64733 26 B2	<input checked="" type="checkbox"/>	Memory structures having selectively disabled portions for power conservation	365/78
52	US 64673 87 B1	<input checked="" type="checkbox"/>	Apparatus and method for propelling a data sensing apparatus into a subsurface formation	89/1.15
53	US 64496 67 B1	<input checked="" type="checkbox"/>	Tree network including arrangement for establishing sub-tree having a logical root below the network's physical root	710/28
54	US 64468 66 B1	<input checked="" type="checkbox"/>	Information reproducing system	235/454
55	US 63973 79 B1	<input checked="" type="checkbox"/>	Recording in a program execution profile references to a memory-mapped active device	717/140

	Docum ent ID	U	Title	Current OR
56	US 63877 09 B1	<input checked="" type="checkbox"/>	Method of operating an optical sensor adapted for selective analyte-sensing contact with a plurality of samples	436/164
57	US 63564 85 B1	<input checked="" type="checkbox"/>	Merging write cycles by comparing at least a portion of the respective write cycle addresses	365/189 .01
58	US 63531 71 B1	<input checked="" type="checkbox"/>	Tone generating method and device	84/603
59	US 63473 85 B1	<input checked="" type="checkbox"/>	Interleavers for turbo code	714/701
60	US 63450 28 B1	<input checked="" type="checkbox"/>	Method for direct recording and playback of multiple data tracks and signals	369/84
61	US 63144 66 B1	<input checked="" type="checkbox"/>	System and method for providing random access to a multimedia object over a network	709/231
62	US 63063 47 B1	<input checked="" type="checkbox"/>	Optical sensor and method of operation	422/58
63	US 62849 63 B1	<input checked="" type="checkbox"/>	Tone generating method and device based on software	84/603
64	US 62821 95 B1	<input checked="" type="checkbox"/>	Packetized data transmissions in a switched router architecture	370/392
65	US 62631 46 B1	<input checked="" type="checkbox"/>	Apparatus for optically generating chaotic random numbers	385/147
66	US 62627 63 B1	<input checked="" type="checkbox"/>	Actual size image display	348/135
67	US 62528 15 B1	<input checked="" type="checkbox"/>	First in first out memory circuit	365/221
68	US 62336 52 B1	<input checked="" type="checkbox"/>	Translation lookaside buffer for multiple page sizes	711/108
69	US 62301 69 B1	<input checked="" type="checkbox"/>	Apparatus with a display magnification changing function of annotation	715/512
70	US 62267 36 B1	<input checked="" type="checkbox"/>	Microprocessor configuration arrangement for selecting an external bus width	712/38
71	US 62055 17 B1	<input checked="" type="checkbox"/>	Main memory control apparatus for use in a memory having non-cacheable address space allocated to DMA accesses	711/117
72	US 61919 93 B1	<input checked="" type="checkbox"/>	First in first out memory circuit	365/221
73	US 61698 46 B1	<input checked="" type="checkbox"/>	Compressed video data recording method and recording apparatus providing improved performance during variable-speed playback	386/109
74	US 61583 64 A	<input checked="" type="checkbox"/>	Embroidery stitch data producing device with selective designation confirming functions	112/102 .5
75	US 61544 07 A	<input checked="" type="checkbox"/>	First in first out memory circuit	365/221
76	US 61249 43 A	<input checked="" type="checkbox"/>	Printing apparatus and method for managing print data in units of a band	358/1.1 7
77	US 61115 92 A	<input checked="" type="checkbox"/>	DMA data transfer apparatus, motion picture decoding apparatus using the same, and DMA data transfer method	345/537
78	US 61086 63 A	<input checked="" type="checkbox"/>	Autonomous relational database coprocessor	707/102

	Docum ent ID	U	Title	Current OR
79	US 61082 43 A	<input checked="" type="checkbox"/>	High-speed random access memory device	365/189 .01
80	US 61081 50 A	<input checked="" type="checkbox"/>	Formation of servo information and method for servo control therefor in disk drive data storage system	360/48
81	US 60918 53 A	<input checked="" type="checkbox"/>	Local area linear dynamic range compression	382/232
82	US 60596 48 A	<input checked="" type="checkbox"/>	Automatic deboning method and apparatus for upper half of poultry carcass	452/135
83	US 60442 25 A	<input checked="" type="checkbox"/>	Multiple parallel digital data stream channel controller	710/52
84	US 60328 63 A	<input checked="" type="checkbox"/>	Method and apparatus for producing machine readable bar code	235/462 .07
85	US 60264 79 A	<input checked="" type="checkbox"/>	Apparatus and method for efficient switching of CPU mode between regions of high instruction level parallism and low instruction level parallism in computer programs	712/24
86	US 60230 16 A	<input checked="" type="checkbox"/>	Tone generator system using computer software	84/604
87	US 60095 23 A	<input checked="" type="checkbox"/>	Information processing apparatus with security checking function	713/200
88	US 59912 67 A	<input checked="" type="checkbox"/>	Fixed-length cell multiplex transmission apparatus, fixed-length cell multiplex transmission method, fixed-length cell transmtting apparatus, fixed-length cell multiplexing apparatus, and fixed-length cell distributing apparatus	370/395 .42
89	US 59810 06 A	<input checked="" type="checkbox"/>	High speed process for making fully-oriented nylon yarns and yarns made thereby	428/34. 2
90	US 59785 44 A	<input checked="" type="checkbox"/>	Video compression coding apparatus and video compression recording/playback apparatus	386/112
91	US 59637 46 A	<input checked="" type="checkbox"/>	Fully distributed processing memory element	712/20
92	US 59580 81 A	<input checked="" type="checkbox"/>	Energy efficient remote control protocol with improved reliability	714/811
93	US 59499 80 A	<input checked="" type="checkbox"/>	Bus interface unit for preventing deadlock	710/112
94	US 59296 31 A	<input checked="" type="checkbox"/>	Method of position sensing utilizing giant magneto resistance elements and solid state switch array	324/207 .21
95	US 59146 22 A	<input checked="" type="checkbox"/>	Pulse-width controller	327/172
96	US 59058 14 A	<input checked="" type="checkbox"/>	One-dimensional time series data compression method, one-dimensional time series data decompression method	382/239
97	US 58726 67 A	<input checked="" type="checkbox"/>	Mechanical operations and volume format identifying system zones for tape media	360/48
98	US 58645 53 A	<input checked="" type="checkbox"/>	Multiport frame exchange system	370/392
99	US 58571 83 A	<input checked="" type="checkbox"/>	Processor for sequential data retrieval from a relational database	707/3
100	US 58386 62 A	<input checked="" type="checkbox"/>	System and method for transferring data	370/230

	Docum ent ID	U	Title	Current OR
101	US 58260 93 A	<input checked="" type="checkbox"/>	Dual function disk drive integrated circuit for master mode and slave mode operations	712/43
102	US 58191 11 A	<input checked="" type="checkbox"/>	System for managing transfer of data by delaying flow controlling of data through the interface controller until the run length encoded data transfer is complete	710/29
103	US 58151 33 A	<input checked="" type="checkbox"/>	Display apparatus	345/95
104	US 58023 99 A	<input checked="" type="checkbox"/>	Data transfer control unit for reducing memory requirements in an information processor by converting bit width of data being transferred between memory and processing parts	710/66
105	US 57783 64 A	<input checked="" type="checkbox"/>	Evaluation of content of a data set using multiple and/or complex queries	707/6
106	US 57710 35 A	<input checked="" type="checkbox"/>	Character generation device	345/471
107	US 57502 15 A	<input checked="" type="checkbox"/>	High speed process for making fully-oriented nylon yarns and yarns made thereby	428/34. 2
108	US 57489 98 A	<input checked="" type="checkbox"/>	Camera	396/88
109	US 57199 41 A	<input checked="" type="checkbox"/>	Method for changing passwords on a remote computer	713/155
110	US 57179 43 A	<input checked="" type="checkbox"/>	Advanced parallel array processor (APAP)	712/20
111	US 57105 98 A	<input checked="" type="checkbox"/>	Picture compressing and expanding apparatus	375/240 .2
112	US 57015 20 A	<input checked="" type="checkbox"/>	Camera	396/48
113	US 56805 50 A	<input checked="" type="checkbox"/>	Digital computer for determining a combined tag value from tag values selectively incremented and decremented reflecting the number of messages transmitted and not received	712/11
114	US 56805 04 A	<input checked="" type="checkbox"/>	Apparatus and method for storing and reproducing digital data	704/201
115	US 56733 88 A	<input checked="" type="checkbox"/>	Memory testing in a multiple processor computer system	714/42
116	US 56436 60 A	<input checked="" type="checkbox"/>	Hollow nylon filaments and yarns	442/19
117	US 56340 13 A	<input checked="" type="checkbox"/>	Bus bridge address translator	710/100
118	US 56300 28 A	<input checked="" type="checkbox"/>	Method of representing graphic data using text	358/1.1 1
119	US 56040 36 A	<input checked="" type="checkbox"/>	Hollow nylon filaments	428/376
120	US 55902 83 A	<input checked="" type="checkbox"/>	Parallel computer system with physically separate tree networks for data and control messages	712/29
121	US 55771 83 A	<input checked="" type="checkbox"/>	Printer system that stores compressed font glyphs in data blocks and decompressed glyphs in each data block together using same decompression procedure	358/1.1 5
122	US 55616 90 A	<input checked="" type="checkbox"/>	High speed variable length code decoding apparatus	375/340
123	US 55599 81 A	<input checked="" type="checkbox"/>	Pseudo static mask option register and method therefor	711/103

	Docum ent ID	U	Title	Current OR
124	US 55588 26 A	<input checked="" type="checkbox"/>	High speed process for making fully-oriented nylon yarns	264/103
125	US 55308 09 A	<input checked="" type="checkbox"/>	Router for parallel computer including arrangement for redirecting messages	709/250
126	US 55191 99 A	<input checked="" type="checkbox"/>	Method and apparatus for detecting and storing bar code data and re-detecting undetected data	235/462 .16
127	US 55172 40 A	<input checked="" type="checkbox"/>	Image pickup apparatus including matrix computing process which uses coefficients from image pick-up output of solid state imager to generate primary color components	348/223 .1
128	US 54974 04 A	<input checked="" type="checkbox"/>	Transmission error recovery for digital communication systems using variable length data packets where data is stored in header locations of memory	375/240 .27
129	US 54932 71 A	<input checked="" type="checkbox"/>	Fire alarm system	340/505
130	US 54692 73 A	<input checked="" type="checkbox"/>	Decoding system having parallel-processing capability therein	382/244
131	US 54594 79 A	<input checked="" type="checkbox"/>	Solid state depth locator having liquid crystal display	345/50
132	US 54403 45 A	<input checked="" type="checkbox"/>	High efficient encoding/decoding system	375/240 .14
133	US 54287 25 A	<input checked="" type="checkbox"/>	System for performing coordinate transformation of source image data	345/648
134	US 54086 10 A	<input checked="" type="checkbox"/>	Communication control apparatus which communicates management information in a communication system	709/245
135	US 53902 98 A	<input checked="" type="checkbox"/>	Parallel computer system including arrangement for quickly draining messages from message router	712/22
136	US 53882 14 A	<input checked="" type="checkbox"/>	Parallel computer system including request distribution network for distributing processing requests to selected sets of processors in parallel	712/15
137	US 53576 22 A	<input checked="" type="checkbox"/>	Apparatus for queing and storing data writes into valid word patterns	711/117
138	US 53511 07 A	<input checked="" type="checkbox"/>	Image forming apparatus and method having image density correcting function	399/49
139	US 53415 06 A	<input checked="" type="checkbox"/>	Data flow processor with a full-to-half word convertor	712/26
140	US 53332 68 A	<input checked="" type="checkbox"/>	Parallel computer system	709/244
141	US 53095 54 A	<input checked="" type="checkbox"/>	Apparatus for converting character outline data into dot data, having means for moving outline segments	345/170
142	US 53013 04 A	<input checked="" type="checkbox"/>	Emulating records in one record format in another record format	703/24
143	US 52935 87 A	<input checked="" type="checkbox"/>	Terminal control circuitry with display list processor that fetches instructions from a program memory, character codes from a display memory, and character segment bitmaps from a font memory	345/553
144	US 52652 12 A	<input checked="" type="checkbox"/>	Sharing of bus access among multiple state machines with minimal wait time and prioritization of like cycle types	710/113
145	US 52652 07 A	<input checked="" type="checkbox"/>	Parallel computer system including arrangement for transferring messages from a source processor to selected ones of a plurality of destination processors and combining responses	712/15

	Docum ent ID	U	Title	Current OR
146	US 52476 38 A	<input checked="" type="checkbox"/>	Apparatus for compressing data in a dynamically mapped virtual data storage subsystem	710/68
147	US 52257 55 A	<input checked="" type="checkbox"/>	Motor servo circuit for disc reproduction apparatus	318/685
148	US 51796 76 A	<input checked="" type="checkbox"/>	Address selection circuit including address counters for performing address selection	711/212
149	US 51795 28 A	<input checked="" type="checkbox"/>	Signal processing system and method implementing a non-linear transfer function approximated by a piecewise linear transformation	708/203
150	US 51577 81 A	<input checked="" type="checkbox"/>	Data processor test architecture	714/30
151	US 51516 93 A	<input checked="" type="checkbox"/>	Wireless signal transmission system with random access	340/5.2 8
152	US 51214 32 A	<input checked="" type="checkbox"/>	Franking machine, with printing device external to secure housing	380/51
153	US 51054 71 A	<input checked="" type="checkbox"/>	Apparatus for converting character outline data into dot data, having means for correcting reproduction width of strokes of rotated or italicized characters	382/297
154	US 50035 10 A	<input checked="" type="checkbox"/>	Semiconductor memory device with flash write mode of operation	365/189 .01
155	US 49875 07 A	<input checked="" type="checkbox"/>	Flexure guide for straight-line motion	360/261 .1
156	US 49673 91 A	<input checked="" type="checkbox"/>	Data string retrieval apparatus for IC card	711/220
157	US 48993 06 A	<input checked="" type="checkbox"/>	Test interface circuit which generates different interface control signals for different target computers responding to control signals from host computer	710/64
158	US 48893 46 A	<input checked="" type="checkbox"/>	Automated target range system	273/406
159	US 48050 46 A	<input checked="" type="checkbox"/>	Information recording and reproducing apparatus using sectors divided into a plurality of frames and having means for proper storage of the frame data	360/49
160	US 47863 53 A	<input checked="" type="checkbox"/>	Laminating method and apparatus with extensible web width control	156/359
161	US 47713 85 A	<input checked="" type="checkbox"/>	Word recognition processing time reduction system using word length and hash technique involving head letters	715/532
162	US 47631 37 A	<input checked="" type="checkbox"/>	Two pass thermal printing	347/199
163	US 47589 75 A	<input checked="" type="checkbox"/>	Data processor capable of processing floating point data with exponent part of fixed or variable length	708/513
164	US 47506 21 A	<input checked="" type="checkbox"/>	Apparatus for sorting cutlery	209/539
165	US 47335 93 A	<input checked="" type="checkbox"/>	Mixed meter metronome	84/484
166	US 47165 36 A	<input checked="" type="checkbox"/>	Measurement calibration	702/100
167	US 46929 22 A	<input checked="" type="checkbox"/>	Method for correcting and detecting errors	714/777
168	US 46637 32 A	<input checked="" type="checkbox"/>	Apparatus for storing and retrieving data in predetermined multi-bit quantities containing fewer bits of data than word length quantities	711/157

	Docum ent ID	U	Title	Current OR
169	US 46350 96 A	<input checked="" type="checkbox"/>	Test signal generator	348/181
170	US 46163 61 A	<input checked="" type="checkbox"/>	Digital signal and multiplex device	370/503
171	US 45947 05 A	<input checked="" type="checkbox"/>	Bus-configured local area network with data exchange capability	370/257
172	US 45918 51 A	<input checked="" type="checkbox"/>	General purpose data control system	340/3.2 1
173	US 44674 11 A	<input checked="" type="checkbox"/>	Scheduling device operations in a buffered peripheral subsystem	711/136
174	US 44583 16 A	<input checked="" type="checkbox"/>	Queuing commands in a peripheral data storage system	710/52
175	US 44505 61 A	<input checked="" type="checkbox"/>	Method and device for generating check bits protecting a data word	714/757
176	US 42662 81 A	<input checked="" type="checkbox"/>	Microprogrammed programmable controller	700/83
177	US 41730 41 A	<input checked="" type="checkbox"/>	Auxiliary microcontrol mechanism for increasing the number of different control actions in a microprogrammed digital data processor having microwords of fixed length	712/226
178	US 40400 02 A	<input checked="" type="checkbox"/>	Well depth matcher utilizing programmable shifting of well logging data	340/854 .1
179	US 40204 84 A	<input checked="" type="checkbox"/>	Segmented character generator for use with a television receiver	345/472
180	US 40041 31 A	<input checked="" type="checkbox"/>	Article carrying coded indicia	235/494
181	US 38645 48 A	<input checked="" type="checkbox"/>	MACHINE FOR READING ARTICLE CARRYING CODED INDICIA	235/462 .18
182	US 38538 69 A	<input checked="" type="checkbox"/>	7-(1-ETHYL-2-METHYLINDOL-3-YL) 7-(SUBSTITUTED)-5,7-DIHYDROFURO (3,4-B) PYRAZIN-5-ONE	544/350
183	US 37470 71 A	<input checked="" type="checkbox"/>	ELECTRONIC DATA PROCESSING SYSTEM	358/1.1 6
184	US 36230 39 A	<input type="checkbox"/>	MAGNETIC TAPE SYSTEM HAVING MARK CODE IN THE FORM OF COINCIDENT ABSENCE OF CLOCK AND PRESENCE OF DATA PULSES	360/48

	Docum ent ID	U	Title	Current OR
1	US 20020 16760 9 A1	<input type="checkbox"/>	TELEVISION CHANNEL SELECTION METHOD AND APPARATUS	348/554
2	US 20020 15017 2 A1	<input checked="" type="checkbox"/>	Caller-ID demodulation apparatus and method using multiple thresholds	375/300
3	US 62627 63 B1	<input checked="" type="checkbox"/>	Actual size image display	348/135
4	US 58023 99 A	<input checked="" type="checkbox"/>	Data transfer control unit for reducing memory requirements in an information processor by converting bit width of data being transferred between memory and processing parts	710/66
5	US 56340 13 A	<input checked="" type="checkbox"/>	Bus bridge address translator	710/100
6	US 51577 81 A	<input checked="" type="checkbox"/>	Data processor test architecture	714/30
7	US 40041 31 A	<input checked="" type="checkbox"/>	Article carrying coded indicia	235/494
8	US 38645 48 A	<input type="checkbox"/>	MACHINE FOR READING ARTICLE CARRYING CODED INDICIA	235/462 .18

	Docum ent ID	U	Title	Current OR
1	JP 20010 84794 A	<input type="checkbox"/>	INTEGRATED CIRCUIT FOR PERFORMING BURN-IN TEST OF AC STRESS AND TESTING METHOD USING THE SAME	
2	JP 20002 01325 A	<input checked="" type="checkbox"/>	DEVICE FOR REPRODUCING SIGNAL RECORDED ON MEDIUM AND METHOD FOR PROCESSING BLOCK ENCODED DIGITAL SIGNAL	
3	JP 20001 13118 A	<input checked="" type="checkbox"/>	IC CARD PROCESSOR, METHOD AND STORAGE MEDIUM WITH CONTROL PROGRAM OF THE SAME DEVICE STORED THEREIN	
4	JP 20000 92450 A	<input checked="" type="checkbox"/>	VIDEO SERVER DEVICE	
5	JP 11355 309 A	<input checked="" type="checkbox"/>	RATE CONTROL COMMUNICATION EQUIPMENT AND RATE CONTROL COMMUNICATION METHOD	
6	JP 11353 319 A	<input checked="" type="checkbox"/>	IMAGE RETRIEVING METHOD	
7	JP 11262 004 A	<input checked="" type="checkbox"/>	DEVICE AND METHOD FOR PROCESSING IMAGE AND STORAGE MEDIUM THEREOF	
8	JP 11238 033 A	<input checked="" type="checkbox"/>	BUS FOR INFORMATION PROCESSOR, AND METHOD AND UNIT FOR BUS CONTROL	
9	JP 11149 437 A	<input checked="" type="checkbox"/>	DATA TRANSFER MEMORY DEVICE	
10	JP 11119 931 A	<input checked="" type="checkbox"/>	PRINTING CONTROLLER	
11	JP 10285 488 A	<input checked="" type="checkbox"/>	VIDEO SIGNAL PROCESSING CIRCUIT	
12	JP 10285 487 A	<input checked="" type="checkbox"/>	VIDEO SIGNAL PROCESSING CIRCUIT	
13	JP 10013 841 A	<input checked="" type="checkbox"/>	METHOD AND DEVICE FOR DECODING PICTURE	
14	JP 10013 390 A	<input checked="" type="checkbox"/>	DUPLICATE TRANSMISSION CHANGEOVER DEVICE	
15	JP 09289 515 A	<input checked="" type="checkbox"/>	ATM CELL GENERATING DEVICE	
16	JP 09246 993 A	<input checked="" type="checkbox"/>	RECEPTION DATA PROCESSING CIRCUIT	
17	JP 09244 607 A	<input checked="" type="checkbox"/>	VIDEO DISPLAY CIRCUIT	
18	JP 09223 173 A	<input checked="" type="checkbox"/>	INFORMATION DISPLAY DEVICE	
19	JP 09083 780 A	<input checked="" type="checkbox"/>	FACSIMILE EQUIPMENT	
20	JP 08331 145 A	<input checked="" type="checkbox"/>	COMMUNICATION INTERFACE DEVICE	
21	JP 08272 994 A	<input checked="" type="checkbox"/>	DEVICE AND METHOD FOR INFORMATION PROCESSING	

	Docum ent ID	U	Title	Current OR
22	JP 08079 494 A	<input checked="" type="checkbox"/>	IMAGE PROCESSING SYSTEM	
23	JP 07162 432 A	<input checked="" type="checkbox"/>	CELL POINTER POINT-OUT SYSTEM	
24	JP 07152 726 A	<input checked="" type="checkbox"/>	MODIFIED CHOLESKY DECOMPOSITION CALCULATING DEVICE	
25	JP 07013 698 A	<input checked="" type="checkbox"/>	DATA TRANSFER CONTROL SYSTEM	
26	JP 06243 338 A	<input checked="" type="checkbox"/>	COPY MANAGING DEVICE	
27	JP 05143 401 A	<input checked="" type="checkbox"/>	USER PROGRAM FILE CONTROLLER	
28	JP 05037 551 A	<input checked="" type="checkbox"/>	DATA TRANSFERRING DEVICE AND METHOD	
29	JP 05026 750 A	<input checked="" type="checkbox"/>	DEVICE FOR MONITORING GAS PRESSURE WITHIN PIPE OF DUCTWORK TRANSMISSION LINE	
30	JP 04316 363 A	<input checked="" type="checkbox"/>	SEMICONDUCTOR MEMORY DEVICE	
31	JP 04205 808 A	<input checked="" type="checkbox"/>	THIN-FILM MAGNETIC HEAD AND MAGNETIC DISK DEVICE	
32	JP 04148 877 A	<input checked="" type="checkbox"/>	WAVEFORM DISPLAY DEVICE	
33	JP 04107 730 A	<input checked="" type="checkbox"/>	FLOATING POINT ADDER	
34	JP 04086 583 A	<input checked="" type="checkbox"/>	FRAME CORRELATION CIRCUIT	
35	JP 04085 644 A	<input checked="" type="checkbox"/>	TRANSMISSION MAXIMUM SEGMENT SIZE SETTING SYSTEM	
36	JP 03183 097 A	<input checked="" type="checkbox"/>	SEMICONDUCTOR MEMORY DEVICE	
37	JP 03030 961 A	<input checked="" type="checkbox"/>	LASER PRINTER	
38	JP 02293 875 A	<input checked="" type="checkbox"/>	COLOR COPYING DEVICE	
39	JP 02214 042 A	<input checked="" type="checkbox"/>	DROP OUT MONITORING DEVICE	
40	JP 02151 898 A	<input checked="" type="checkbox"/>	ELECTRONIC MUSICAL INSTRUMENT	
41	JP 02097 162 A	<input checked="" type="checkbox"/>	FACSIMILE TRANSMISSION CONTROL SYSTEM	
42	JP 01285 359 A	<input checked="" type="checkbox"/>	INK JET RECORDING METHOD	
43	JP 01167 863 A	<input checked="" type="checkbox"/>	IMAGE RECORDER	
44	JP 01142 996 A	<input checked="" type="checkbox"/>	PORTABLE ELECTRONIC DEVICE	

	Document ID	U	Title	Current OR
45	JP 01031 252 A	<input checked="" type="checkbox"/>	DATA BUS WIDTH TRANSFORMER	
46	JP 63282 551 A	<input checked="" type="checkbox"/>	COMMUNICATION SYSTEM BETWEEN HOST COMPUTER AND TERMINAL DEVICE	
47	JP 63263 575 A	<input checked="" type="checkbox"/>	IMAGE INPUT METHOD	
48	JP 62133 858 A	<input checked="" type="checkbox"/>	THERMAL HEAD DRIVING DEVICE	
49	JP 62100 669 A	<input checked="" type="checkbox"/>	IC TESTER	
50	JP 62096 871 A	<input checked="" type="checkbox"/>	IC TESTING APPARATUS	
51	JP 61279 575 A	<input checked="" type="checkbox"/>	ELECTRONIC TYPEWRITER	
52	JP 61182 965 A	<input checked="" type="checkbox"/>	THERMAL PRINTER	
53	JP 61083 048 A	<input checked="" type="checkbox"/>	IMAGE DATA ROTARY APPARATUS	
54	JP 55063 423 A	<input checked="" type="checkbox"/>	DATA TRANSFER SYSTEM	
55	WO 99380 01 A1	<input checked="" type="checkbox"/>	OPTICAL SENSOR AND METHOD OF OPERATION	
56	EP 35257 2 A2	<input checked="" type="checkbox"/>	Semiconductor memory device with flash write mode of operation.	
57	EP 13088 09 A	<input checked="" type="checkbox"/>	Synchronization control method for numerically controlled device, involves positioning slave axis in accordance with position of master axis based on displacement data of specified execution stages	
58	KR 20020 69822 A	<input checked="" type="checkbox"/>	Device for measuring mean square error of VSB receiving system	
59	KR 20020 32689 A	<input checked="" type="checkbox"/>	Apparatus for controlling data width of semiconductor memory	
60	US 63144 66 B	<input checked="" type="checkbox"/>	Multimedia object random access method for streaming media system, involves determining size of data to be sent to client based on size of selected segment and following segments	
61	KR 20010 58087 A	<input checked="" type="checkbox"/>	Write control circuit of semiconductor memory	
62	JP 20011 28166 A	<input checked="" type="checkbox"/>	Integrated circuit for image compression in electronic camera, has dummy data generator to generate dummy data depending on preset data length of application segment	
63	EP 99313 3 A	<input checked="" type="checkbox"/>	Data communications apparatus especially for UMTS - converts data frames into transport data blocks in proportion to relative importance of parts of data	
64	JP 11127 296 A	<input checked="" type="checkbox"/>	Data length controller in image recording devices e.g. facsimile - judges effective length of drawing data of following page along sub-scanning direction, when recording of drawing data is interrupted	

	Docum ent ID	U	Title	Current OR
65	WO 98253 74 A	<input checked="" type="checkbox"/>	Digital message transmission method - forming one or several repetition data messages in transmitting arrangement, in response to specific information data part requested by reception arrangement	
66	JP 09233 476 A	<input checked="" type="checkbox"/>	Image data processor - has judgment unit that judges current variable length decoding position in effective area based on output signals of comparator	
67	JP 09212 600 A	<input checked="" type="checkbox"/>	IC card insertion and removal controller for portable type data processor - outputs IC card insertion/removal event generation interruption signal to system control part and performs detection control of insertion/removal state of IC card into each slot	
68	EP 76863 1 A	<input checked="" type="checkbox"/>	Remote control system and transmission protocol for vehicle applications - receives transmission, converts it to received code based on data length of each transmission segment, compares code to stored code in memory, if it matches function is enabled	
69	EP 58732 4 B	<input checked="" type="checkbox"/>	Variable bit length image data formatting appts. for compression - detects whether data have been stored in all bits of buffer and outputs fixed length data word accordingly	
70	EP 57907 5 A	<input checked="" type="checkbox"/>	Transmission error recovery for digital communications systems e.g compressed video data - assigning separate identifiers to data packets, each designating location in header memory for storing header associated with data packet	
71	EP 56284 5 A	<input checked="" type="checkbox"/>	Video recorder having forward and reverse high speed playback modes - rearranges parts of variable length coded data for recording onto tracks of recording medium to enable playback at specific speeds	
72	EP 30163 6 A	<input checked="" type="checkbox"/>	Transceiver arrangement for full-duplex data transmission - has echo canceller and hybrid junction connecting transmitter receiver and two-wire parts	
73	EP 26250 6 A	<input checked="" type="checkbox"/>	Two pass thermal printing system - controls heat accumulation in printed head by examining line data in memory and feeds ribbon only in blocks being printed	
74	EP 11550 7 A	<input checked="" type="checkbox"/>	Speech and data communication system - is voice band compatible and provides addressing capability together with data transfer using fixed length signalling packets	
75	WO 82037 41 A	<input checked="" type="checkbox"/>	Spatial image transforming system e.g. for real time TV processing - has motion detector comparing corresp pixels on lines of last and last but one field fed to de-interlacing circuit with motion signal	
76	US 42662 81 A	<input checked="" type="checkbox"/>	Micro-programmed programmable controller for e.g. machine tools - has processor and special purpose hardware to execute macro-instructions with ancillary decimal adjust accumulator and bit pointer circuit	
77	BE 88417 2 A	<input type="checkbox"/>	Reproduction of video discs - by projecting image of video master onto silver halide emulsion and processing to give reflective silver particle surface layer	